

100 ↗

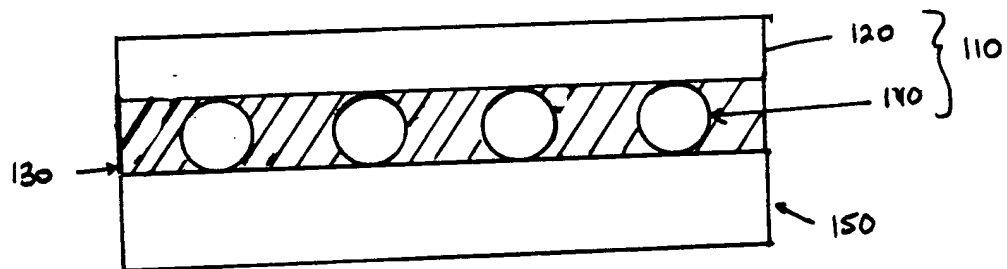
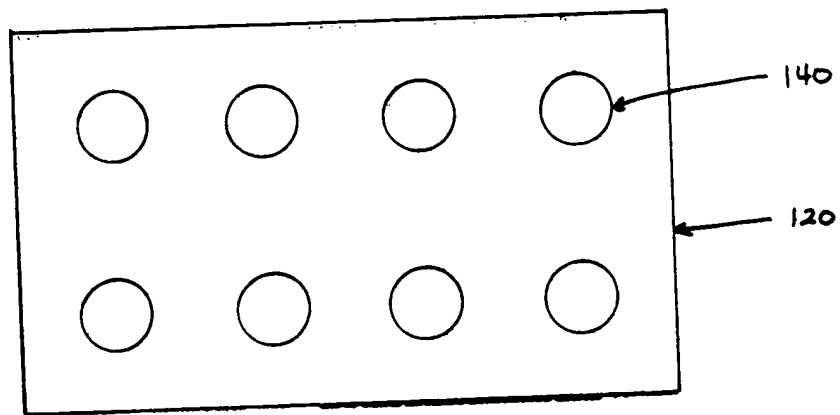


FIG. 1

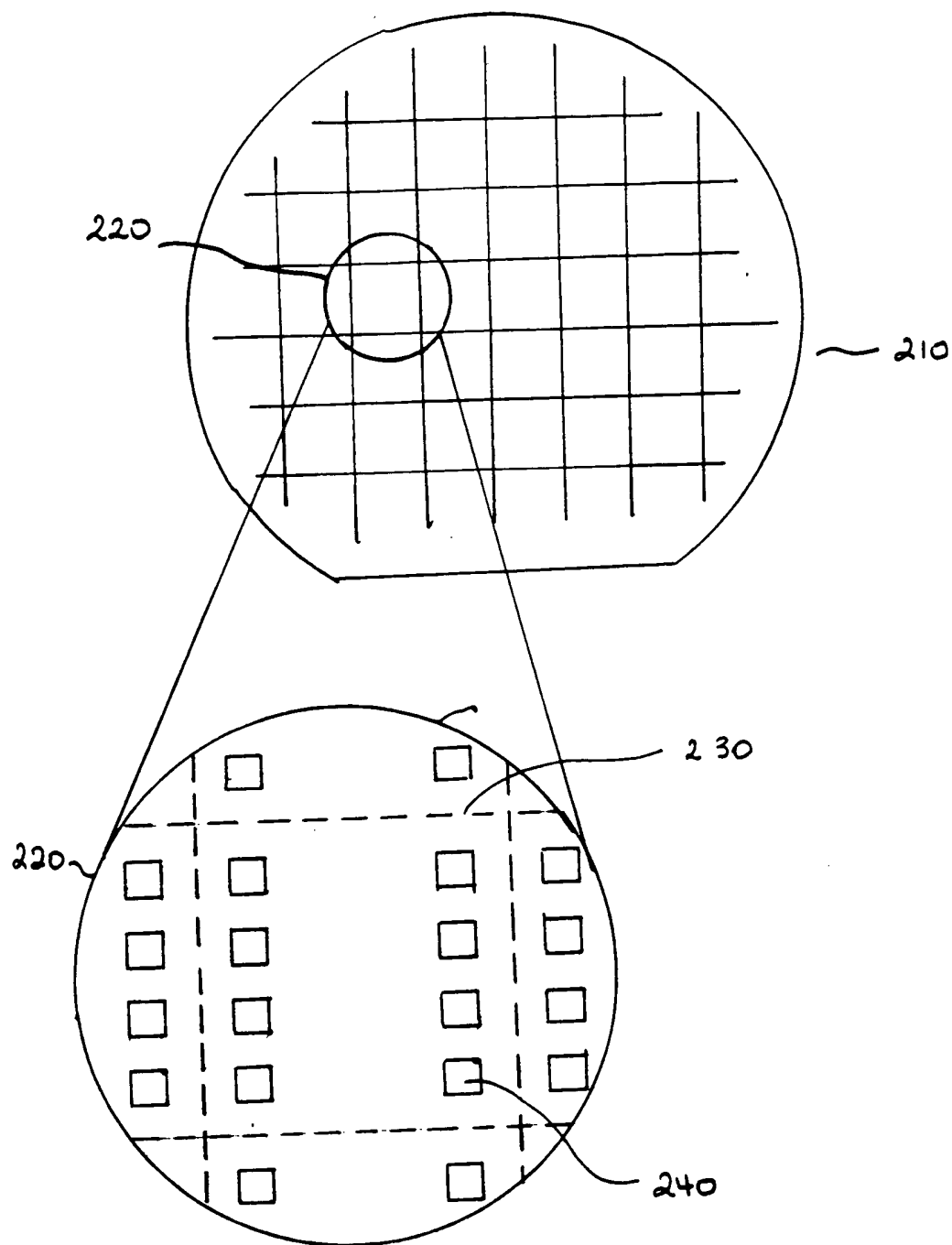


FIG. 2

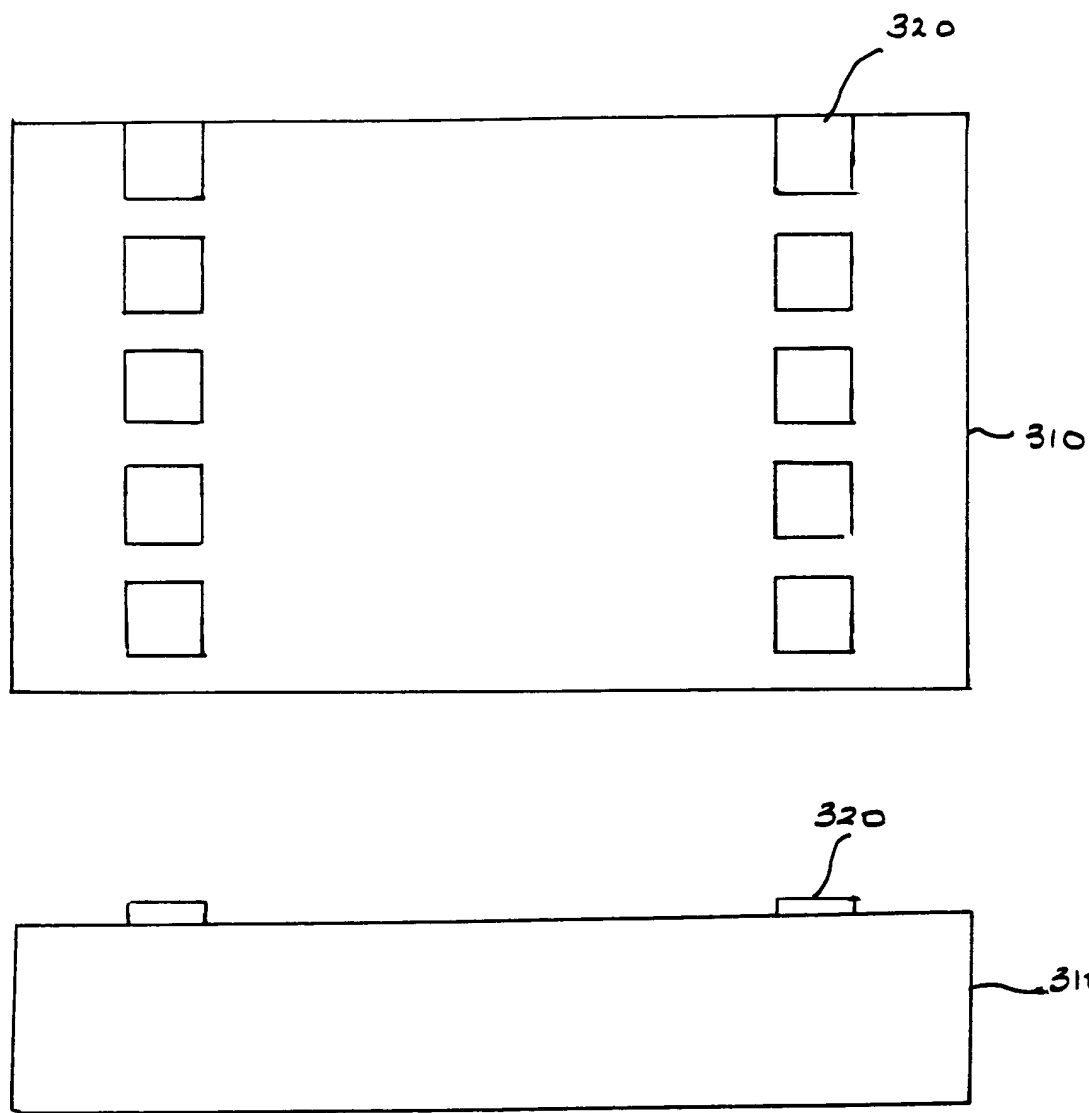


FIG. 3

FIG. 4 is a perspective view of the integrated circuit 300 of FIG. 3, showing the top surface of the integrated circuit 300. The integrated circuit 300 is a rectangular chip with a central region 320 and a peripheral region 410. The peripheral region 410 contains a series of rectangular openings 320 along the top and bottom edges. The central region 320 is a solid, dark, textured area.

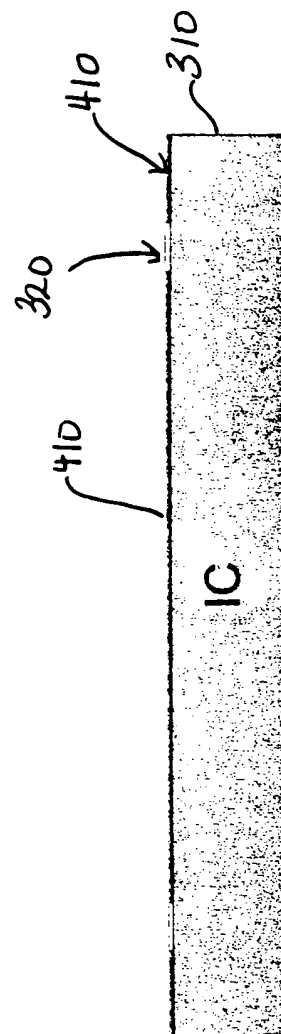
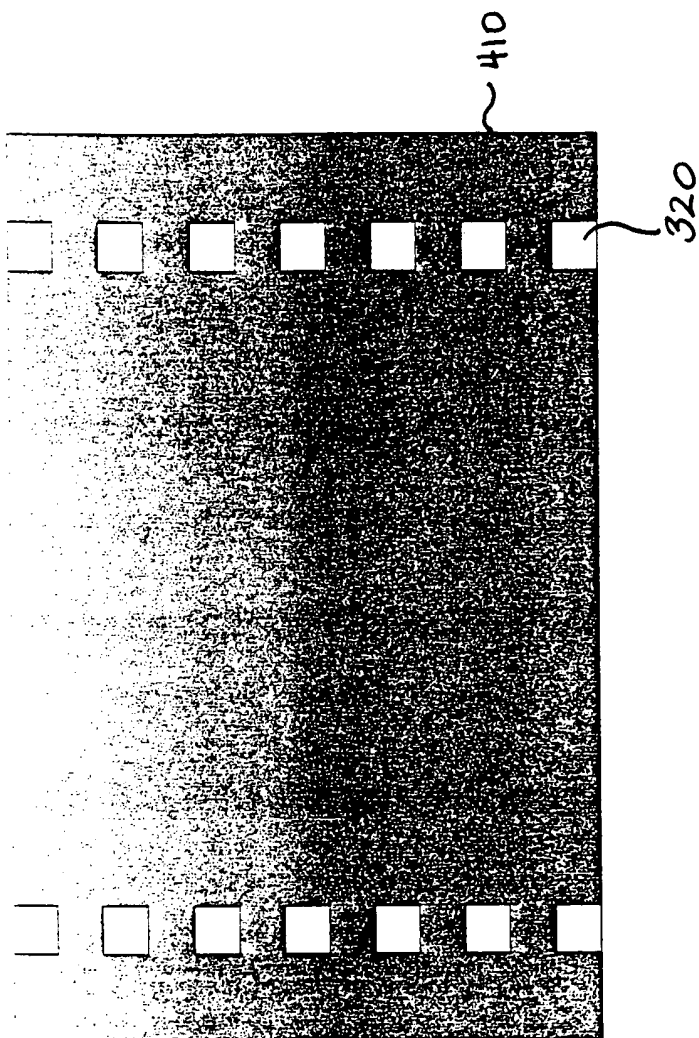


FIG. 4

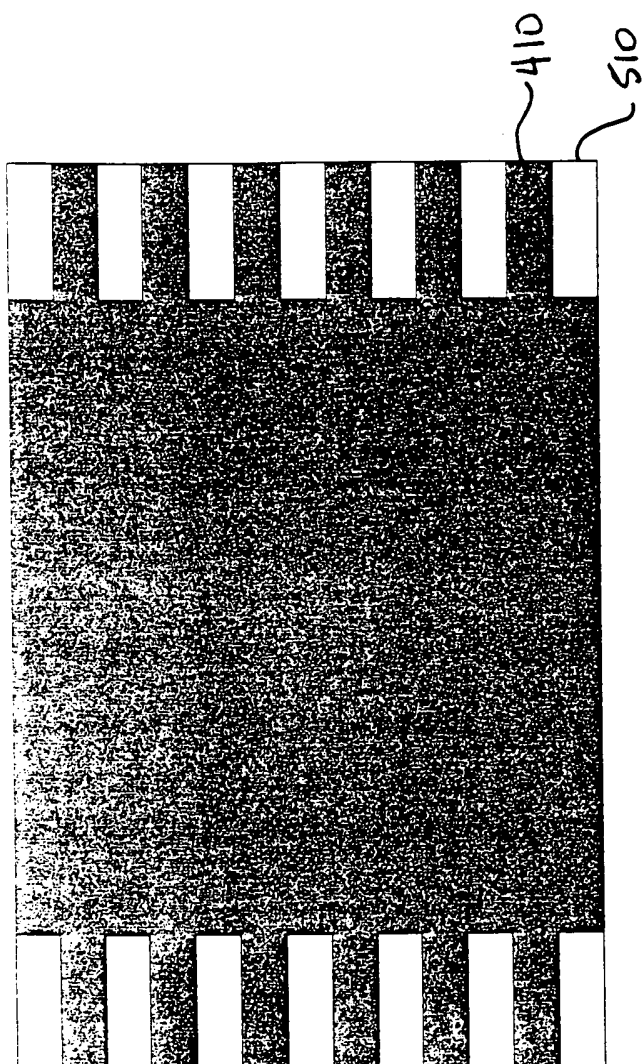


FIG. 5

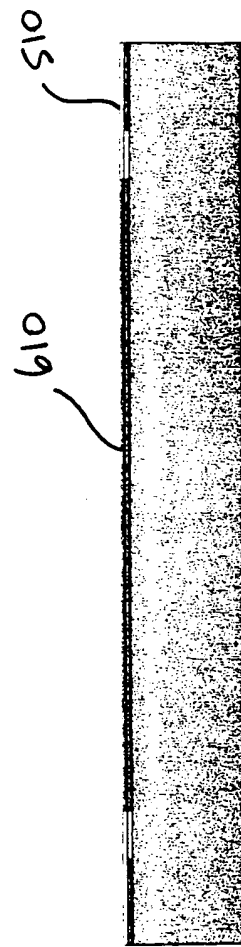
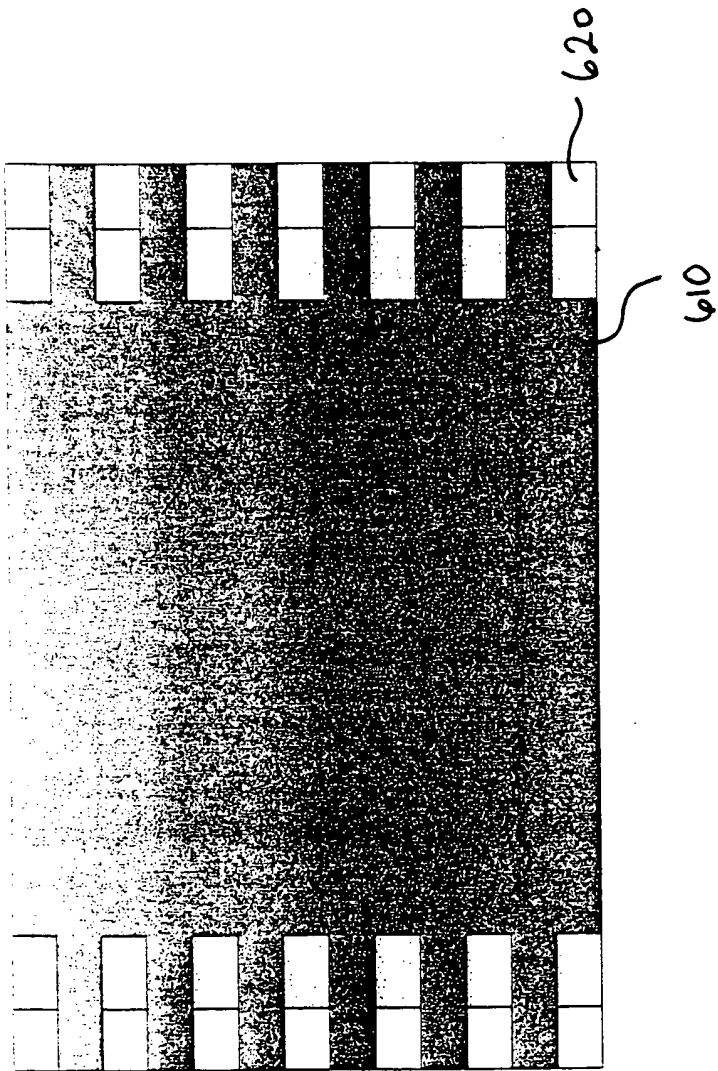


FIG. 6

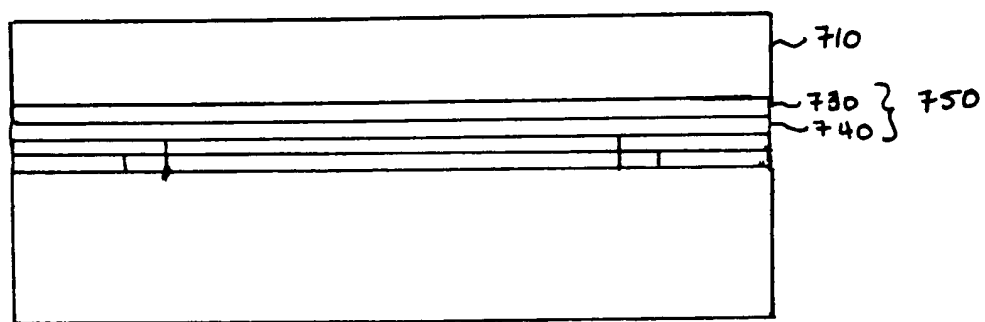
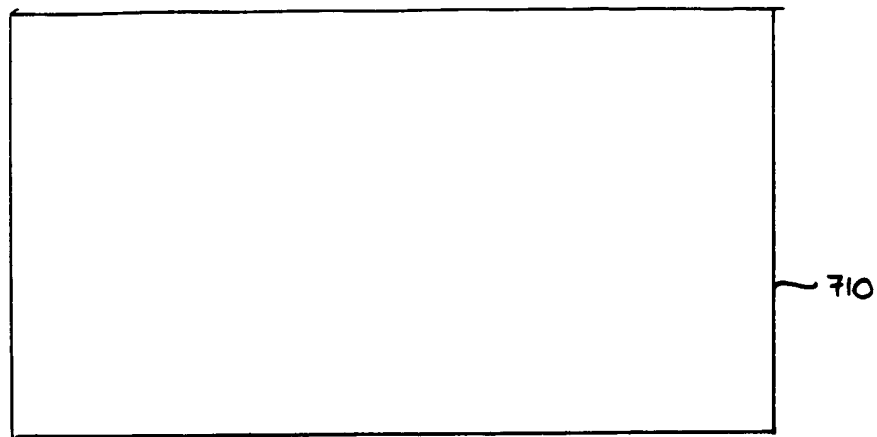
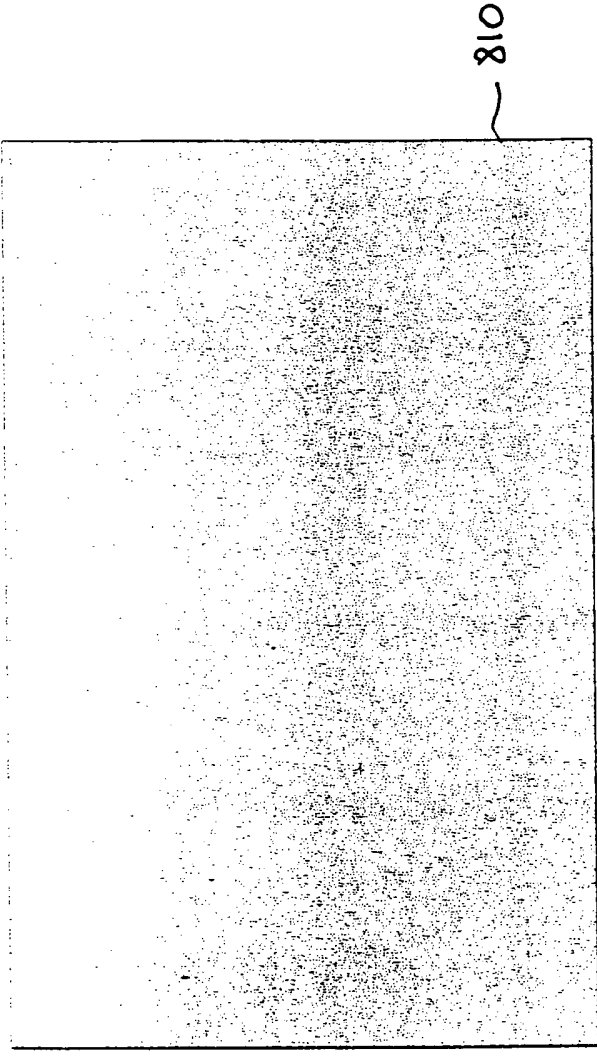
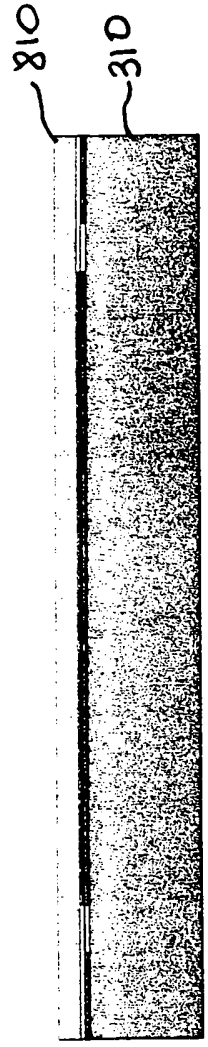


FIG. 7



810



810

310

FIG. 8

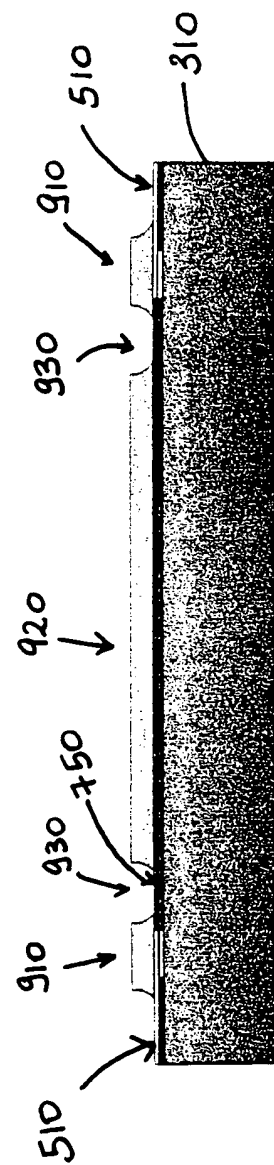
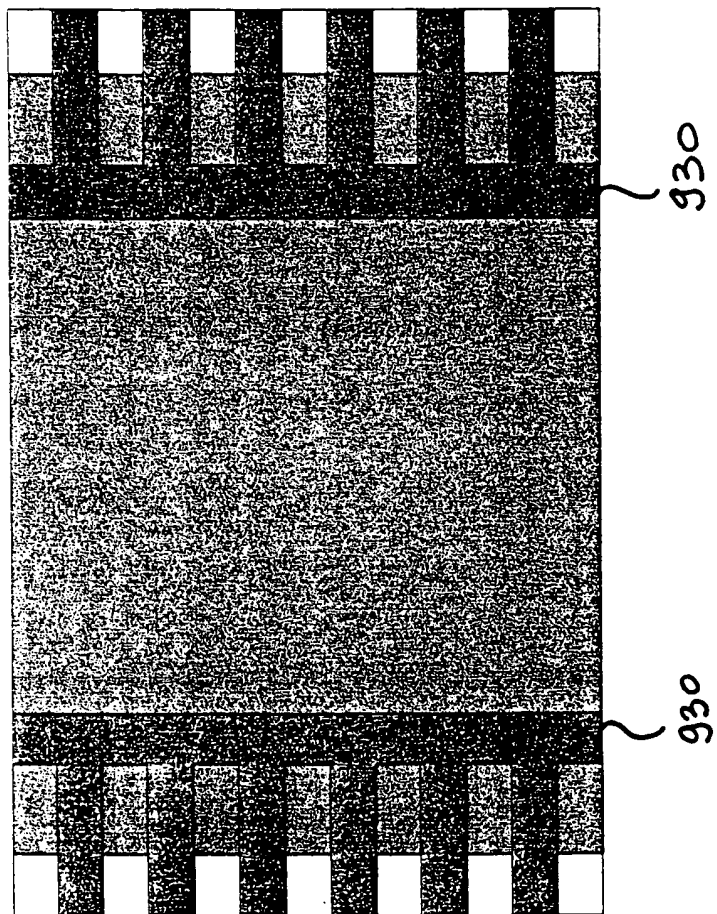


FIG. 9

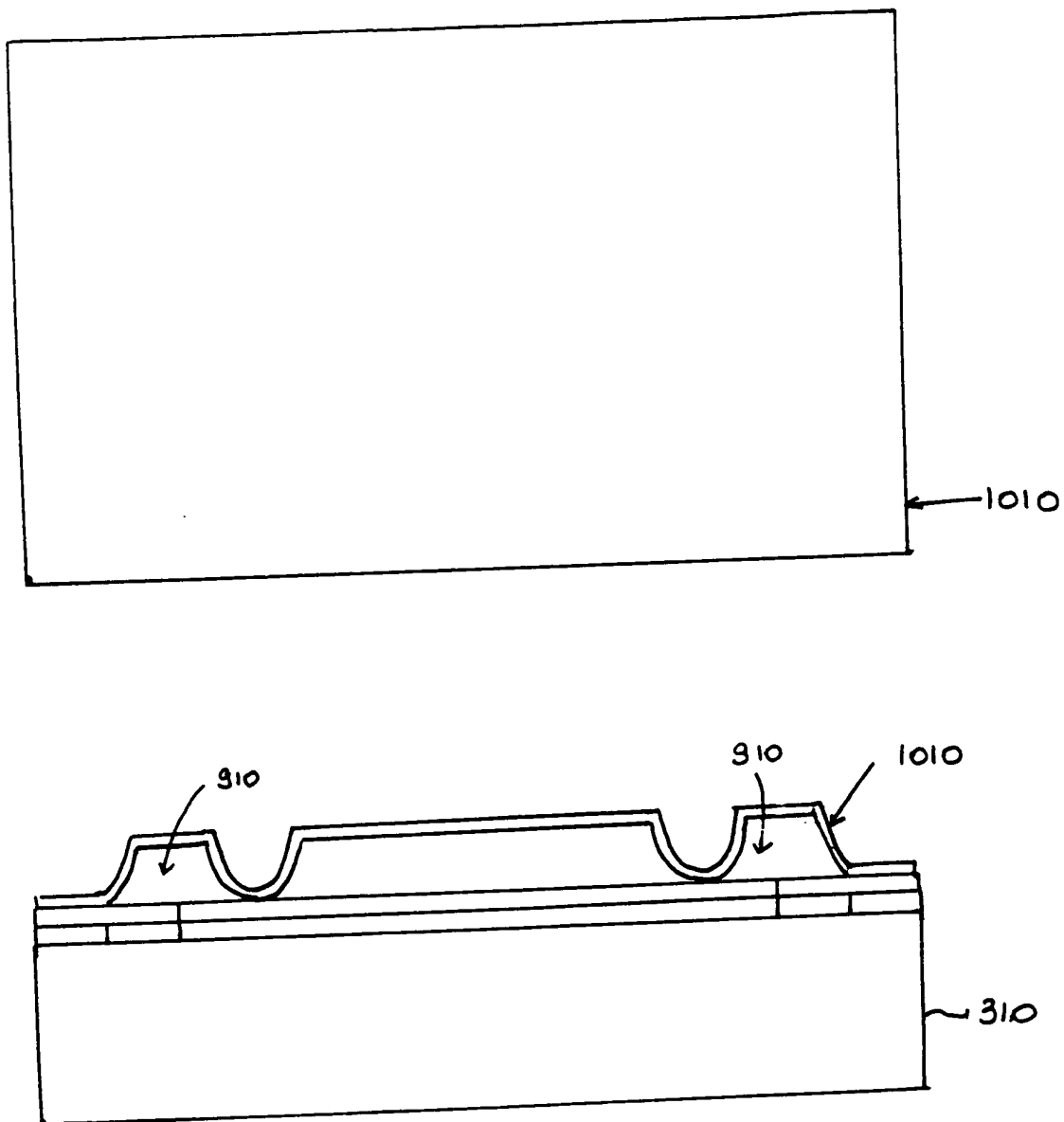


FIG. 10

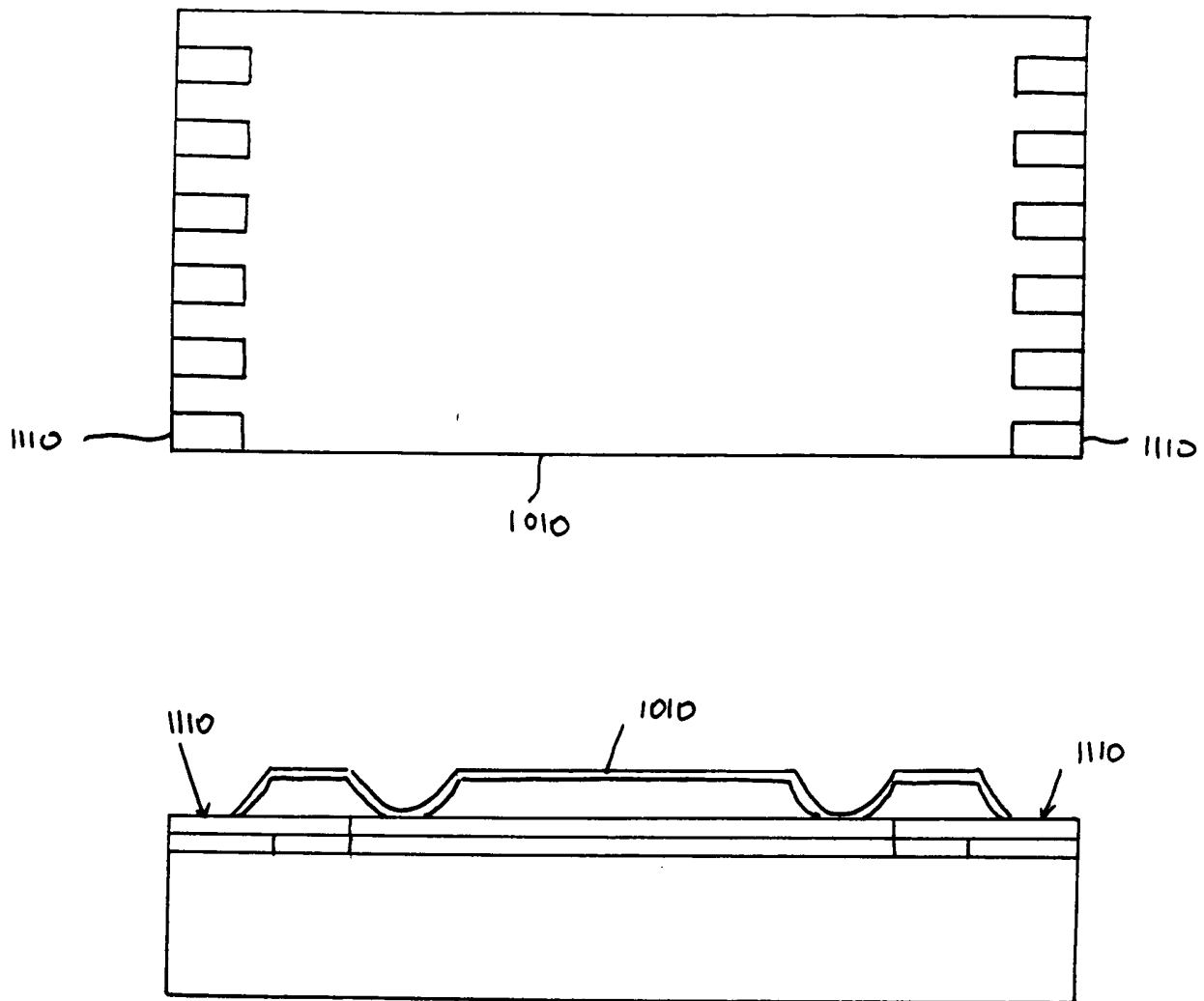


FIG. 11

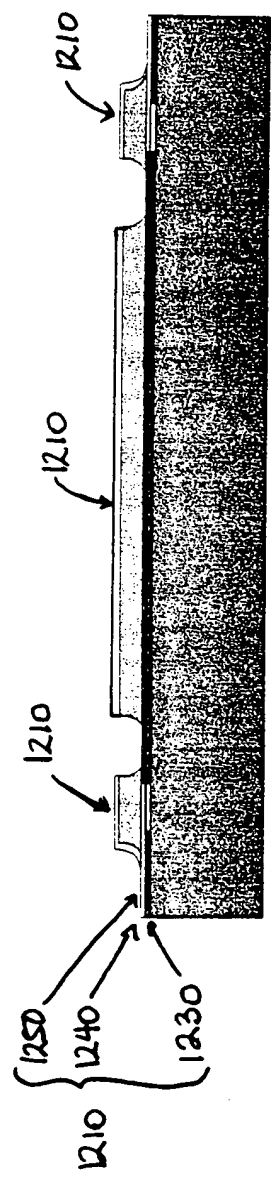
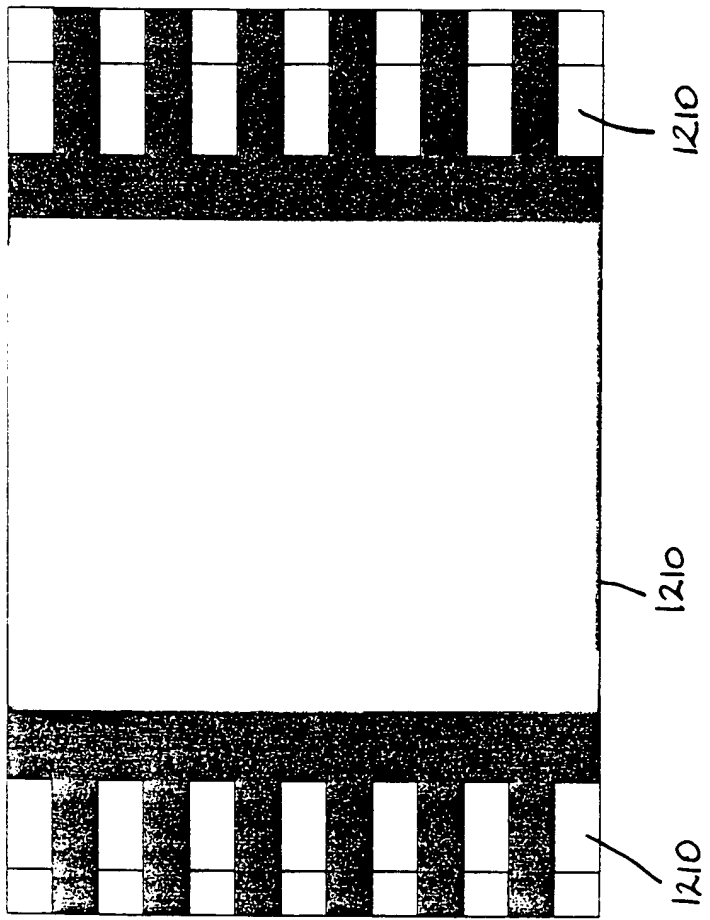


FIG. 12

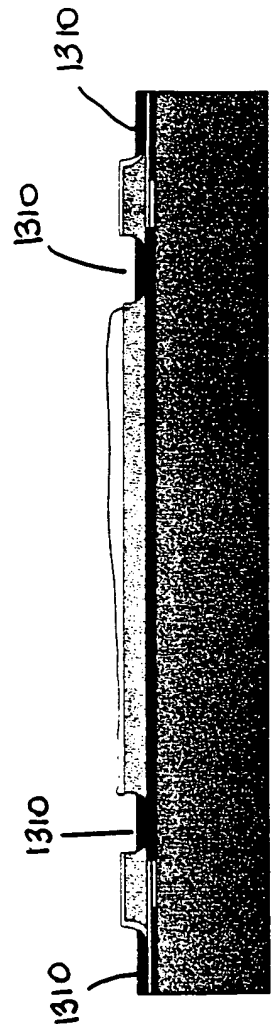
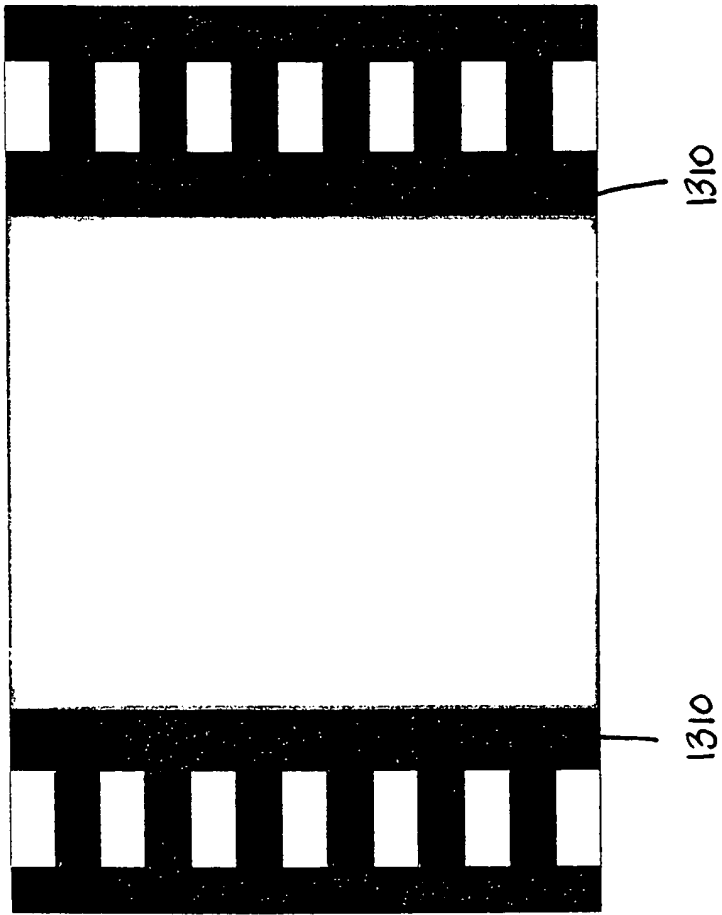


FIG. 13

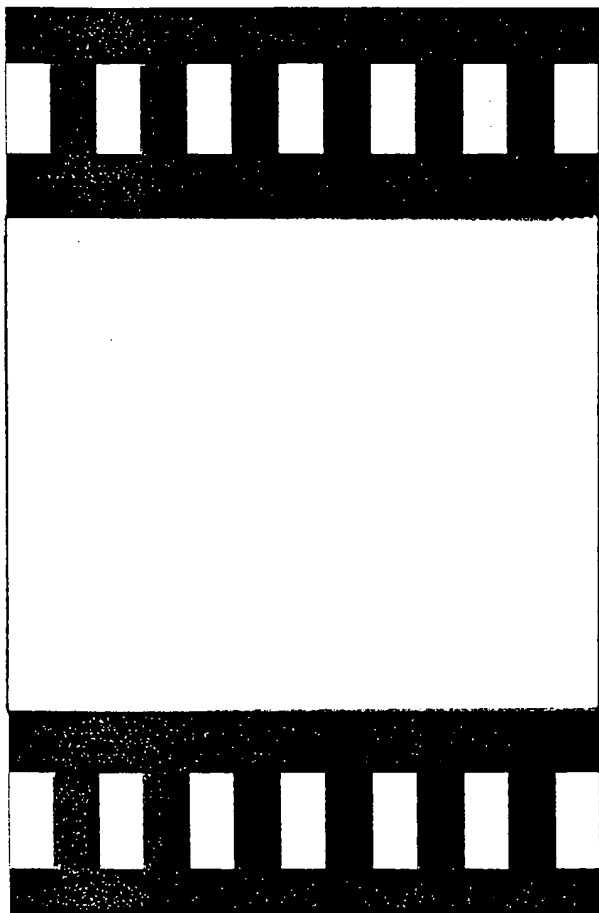
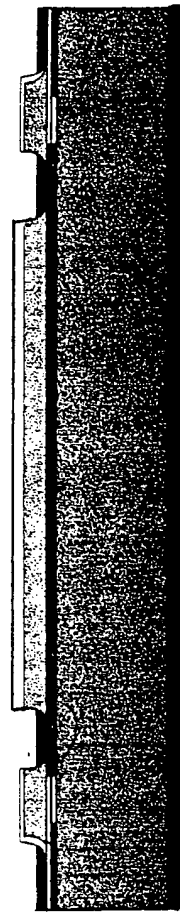


FIG. 14



1410

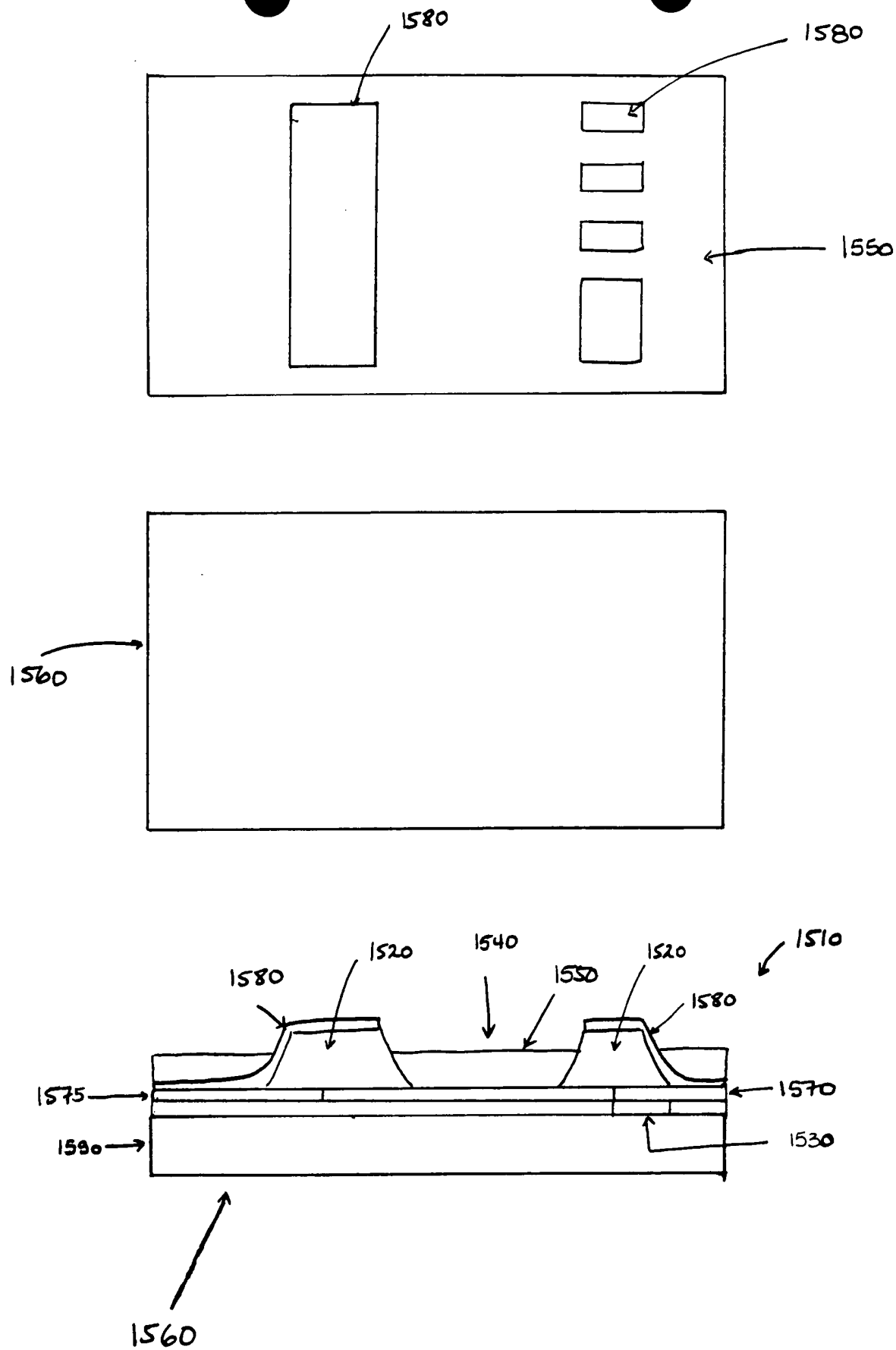


FIG. 15

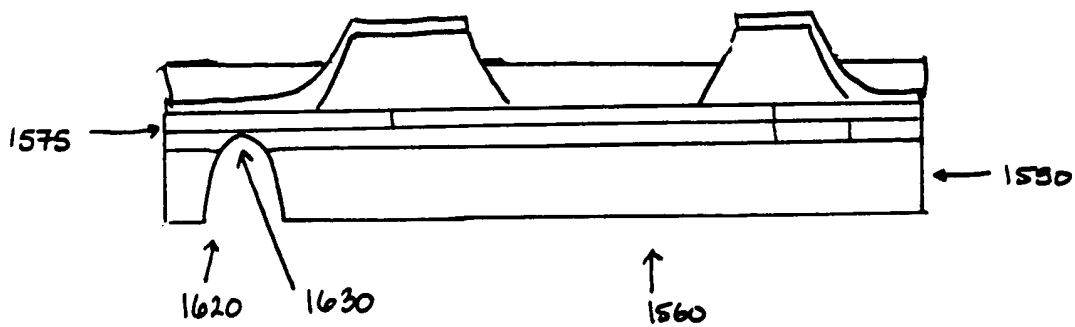
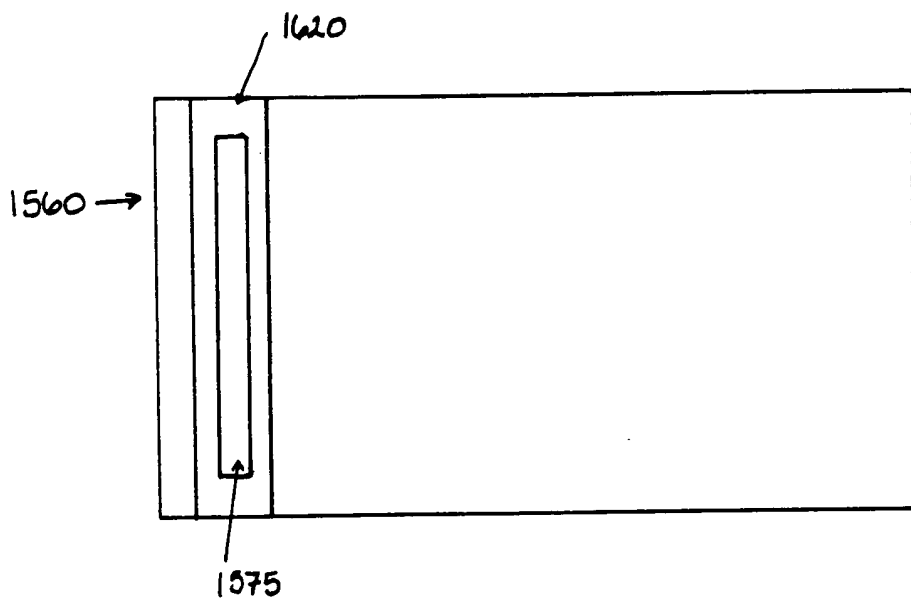
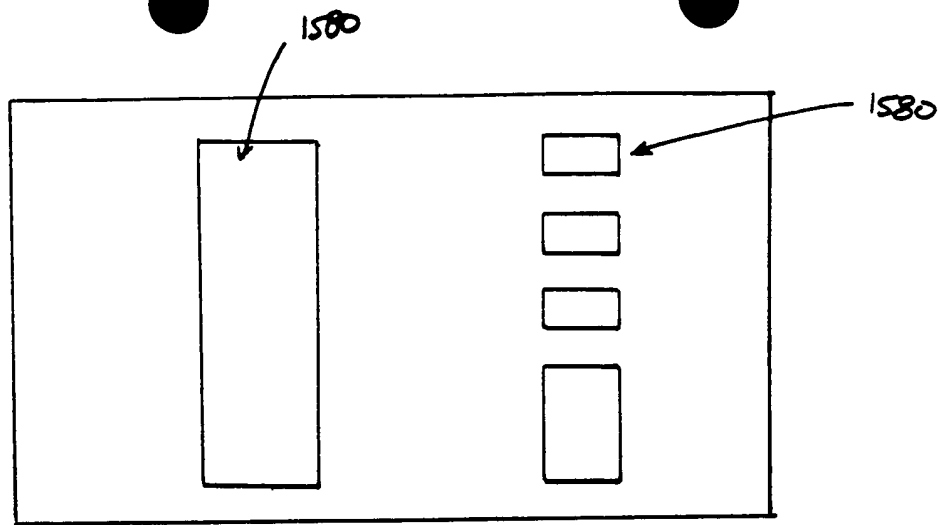


FIG. 16

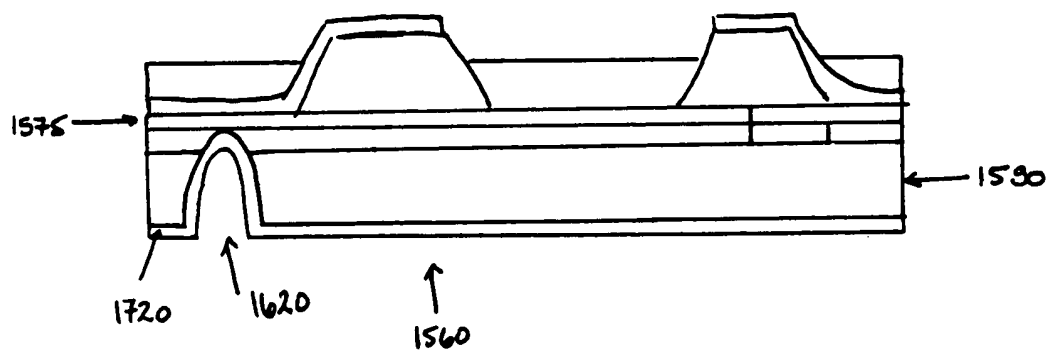
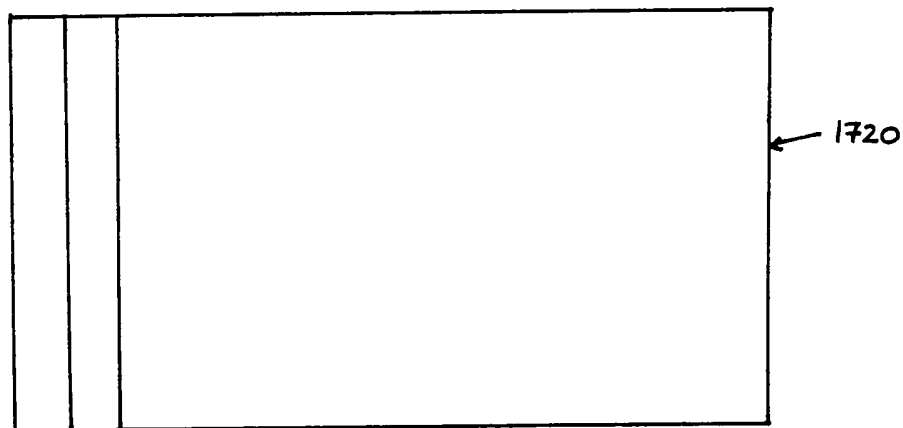
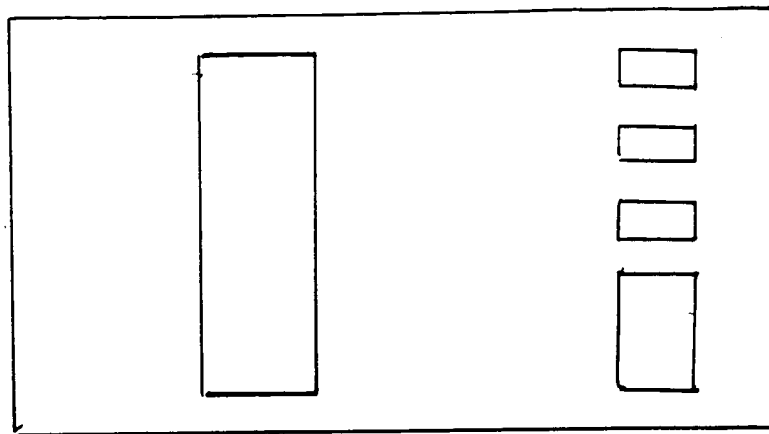


FIG. 17

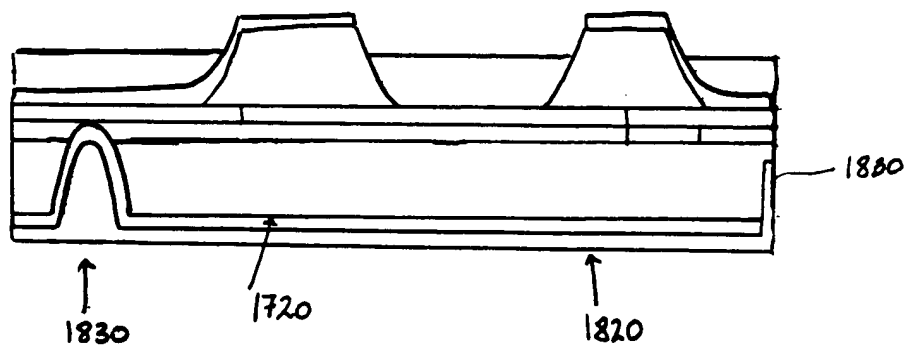
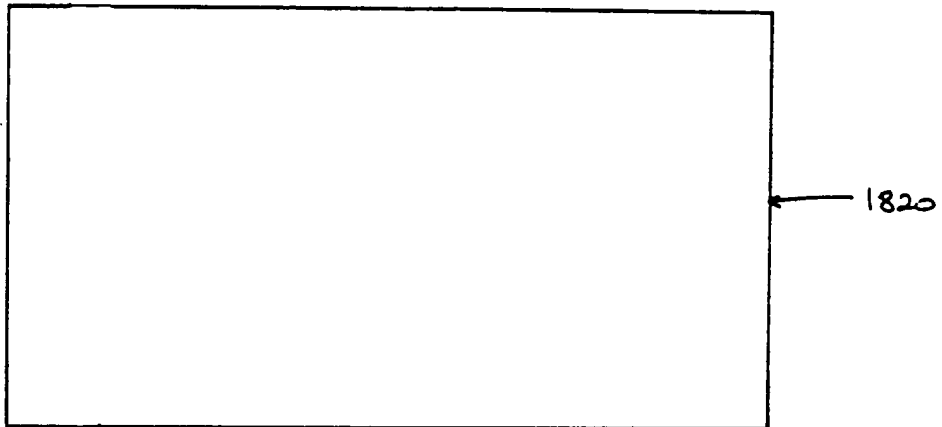
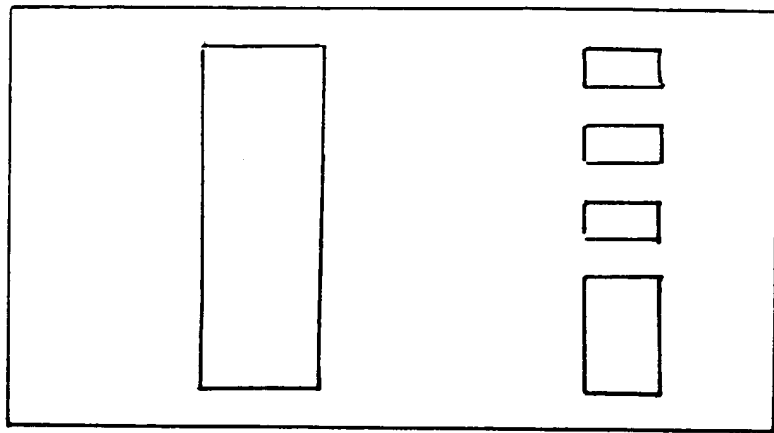


FIG. 18

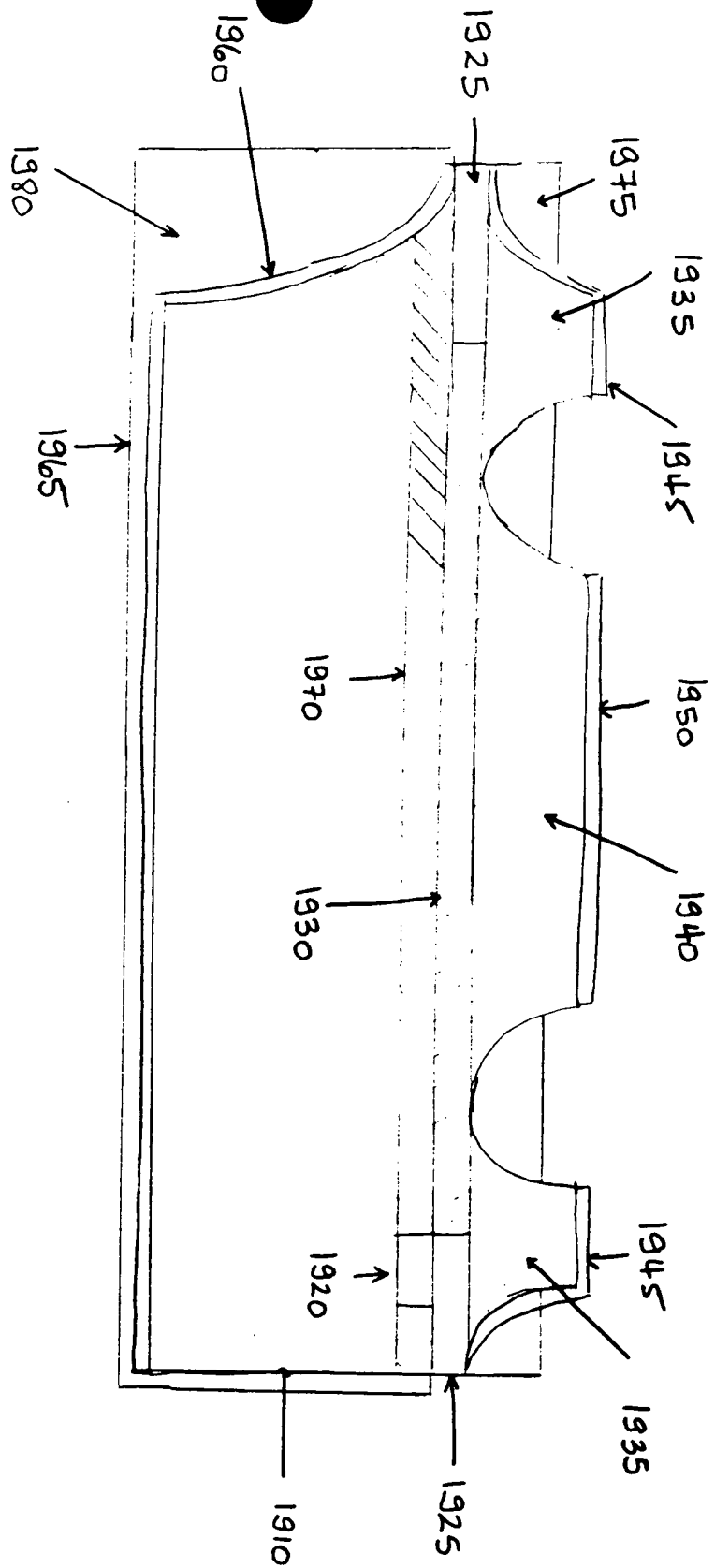


FIG. 19

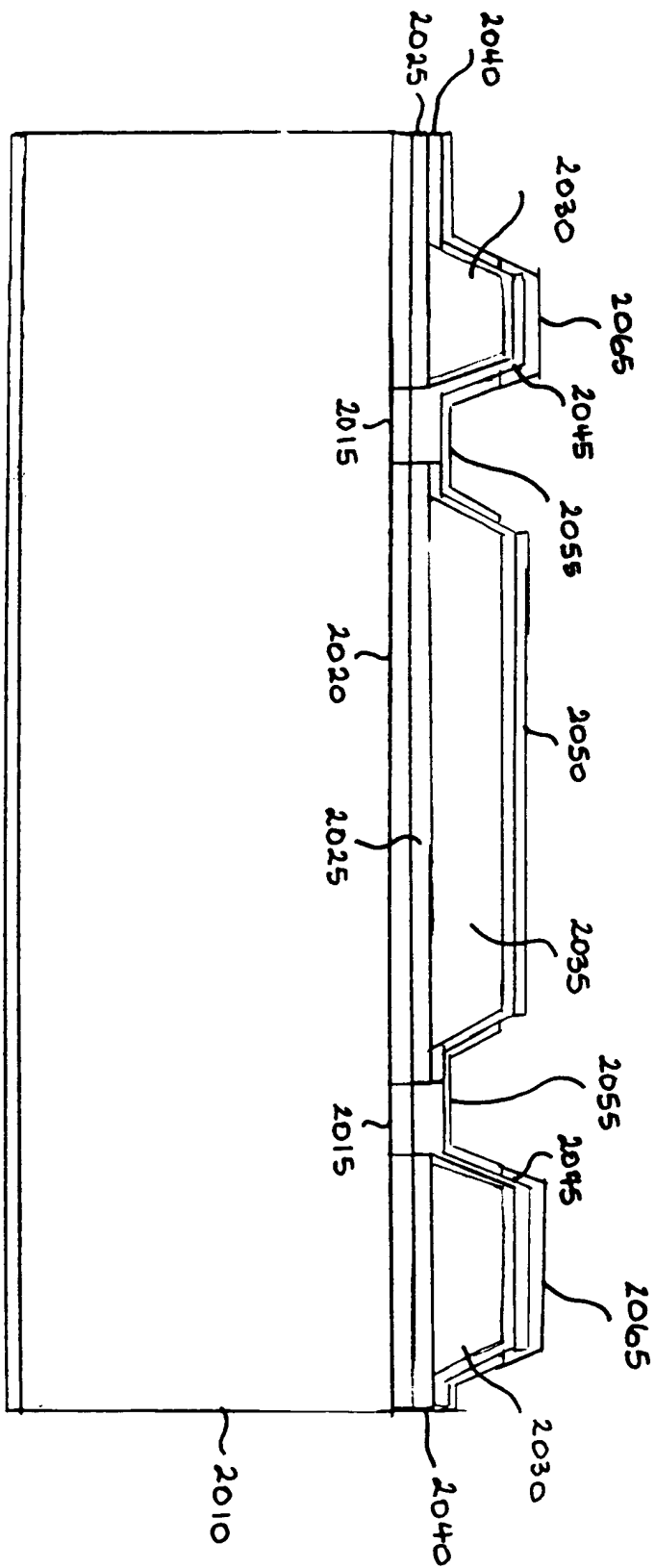


FIG. 20

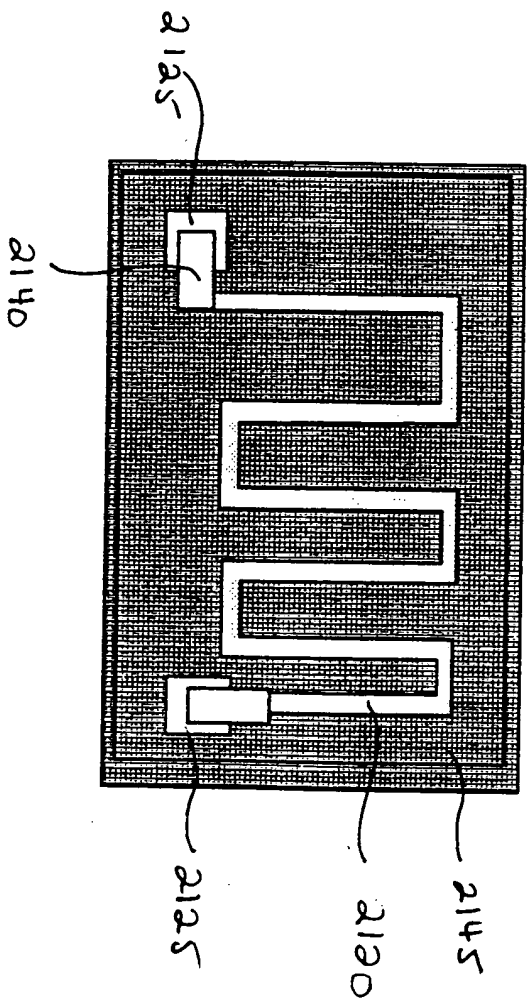
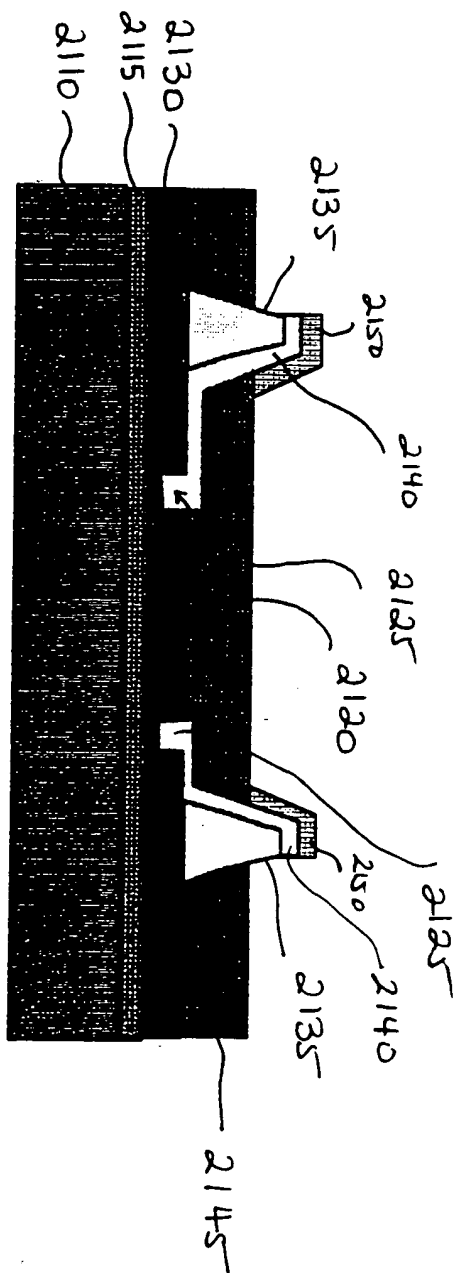


FIG. 21

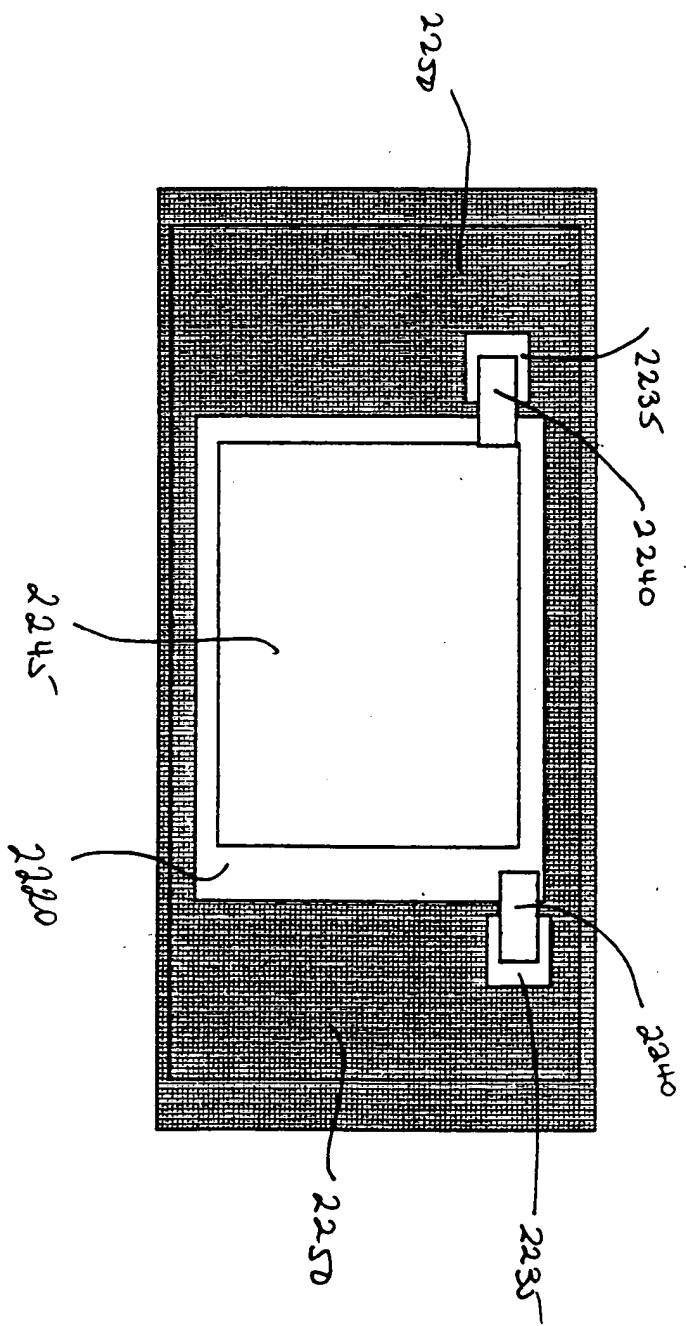
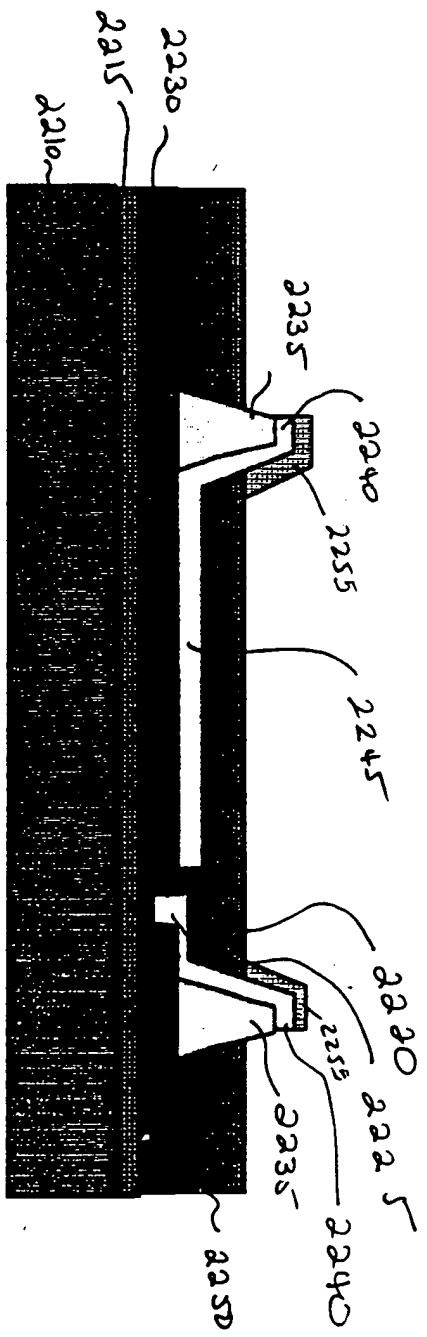


FIG. 22

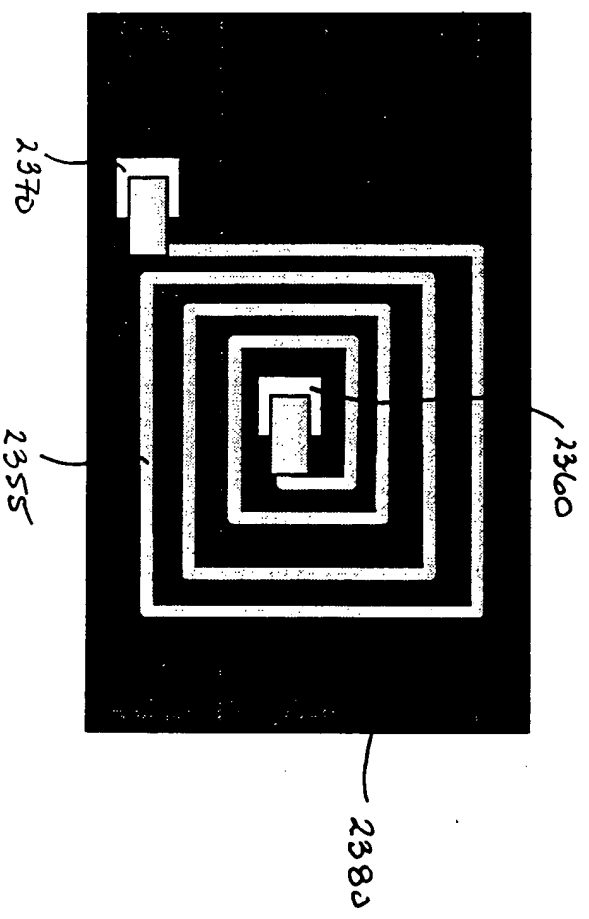
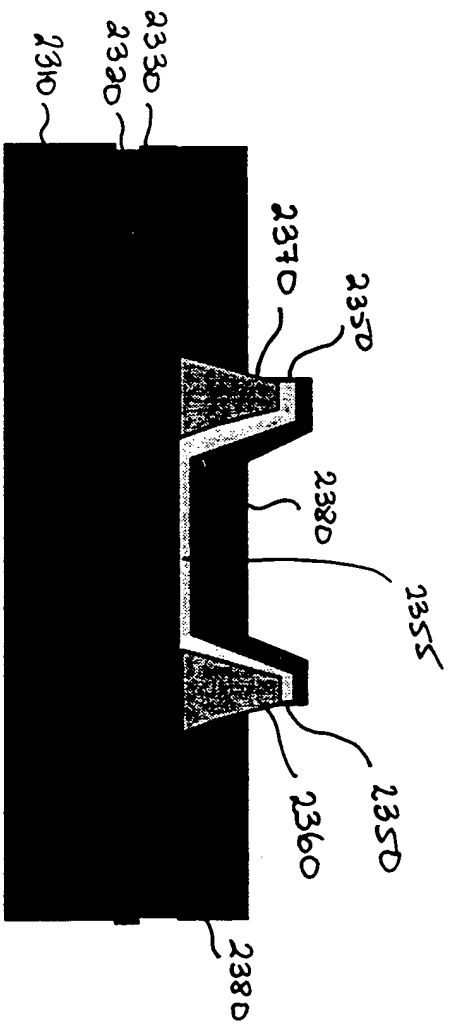


FIG. 23

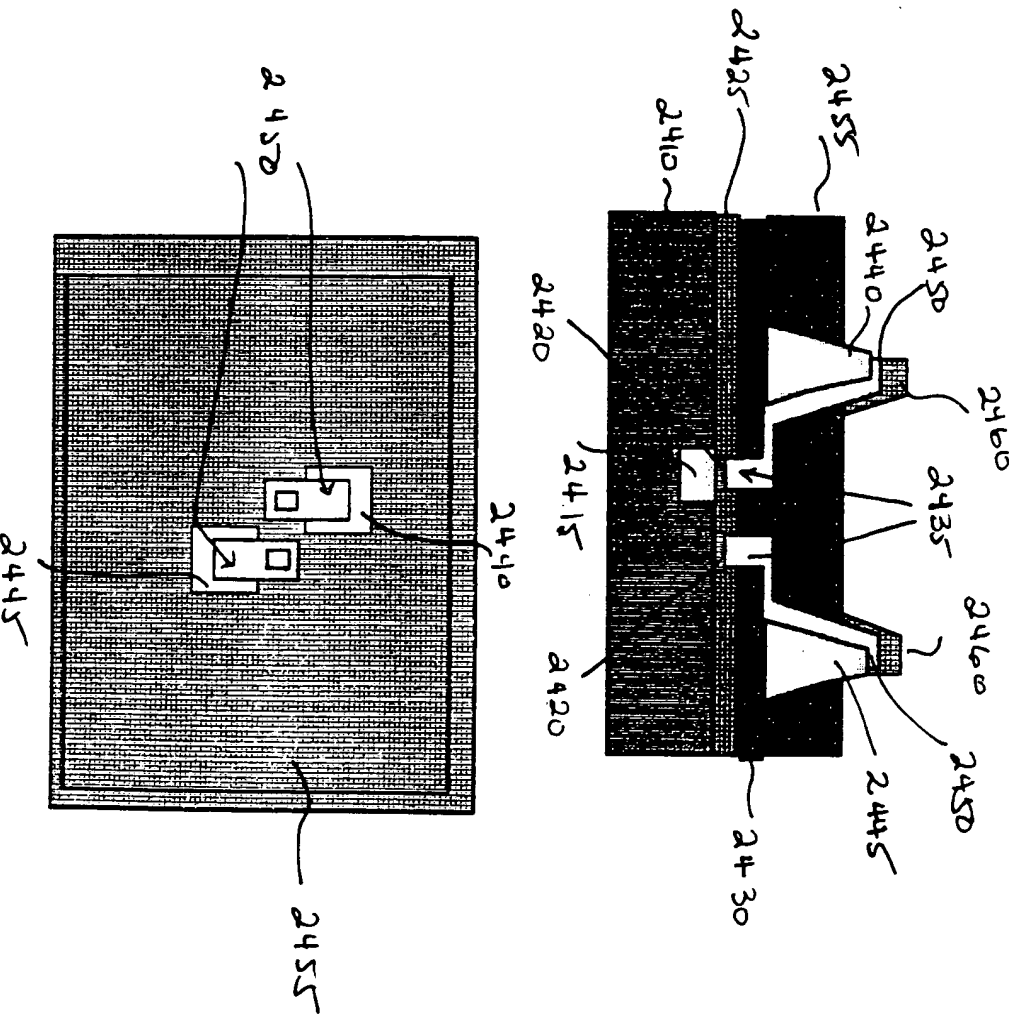
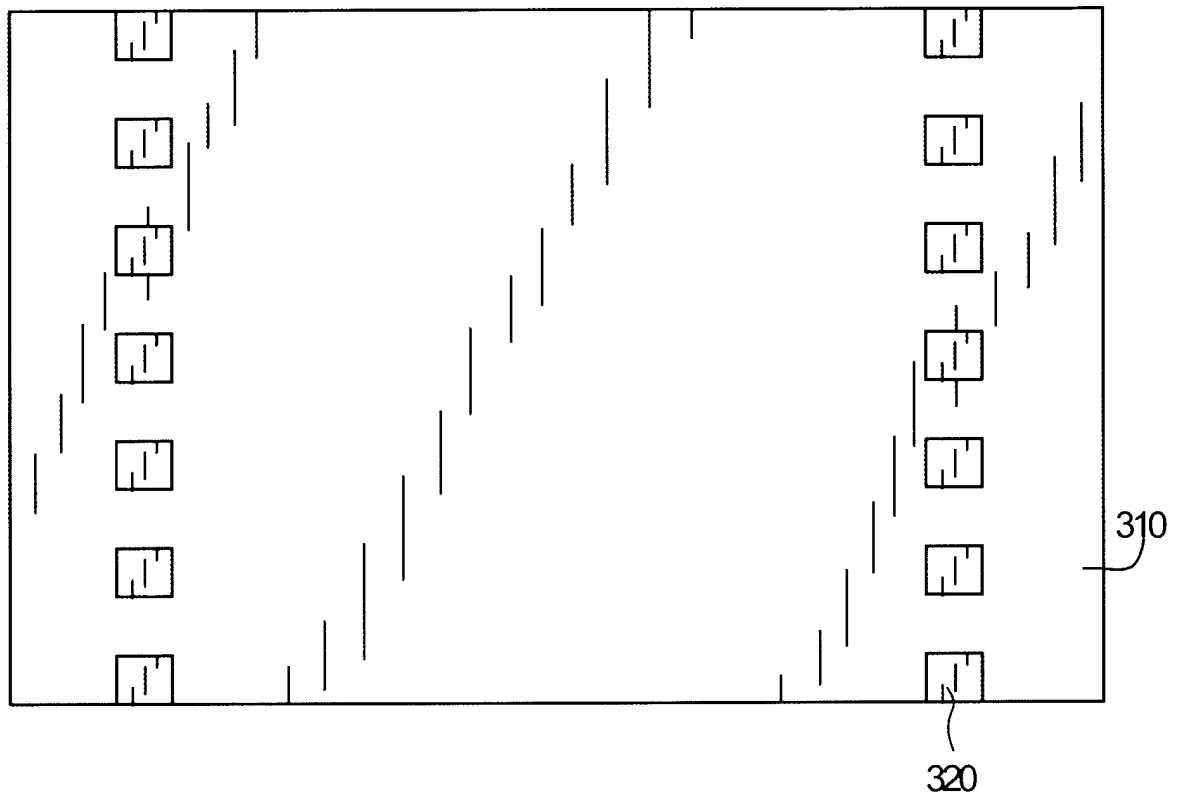
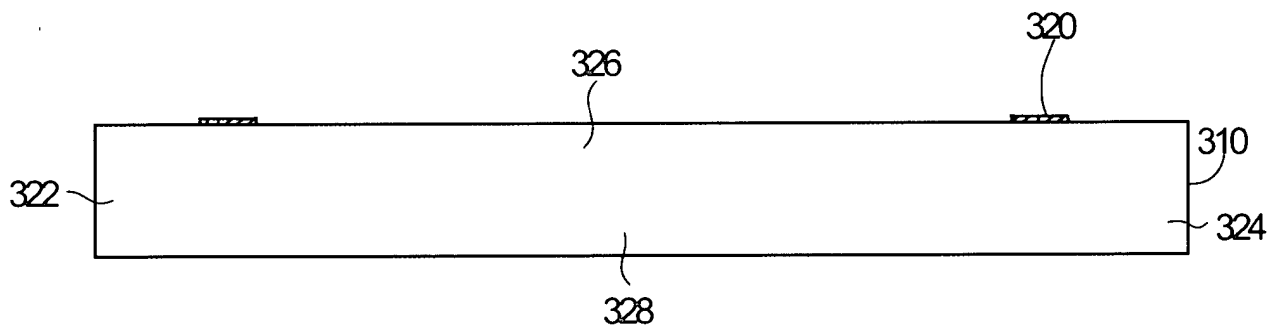


FIG 24

**FIG. 3A****FIG. 3B**

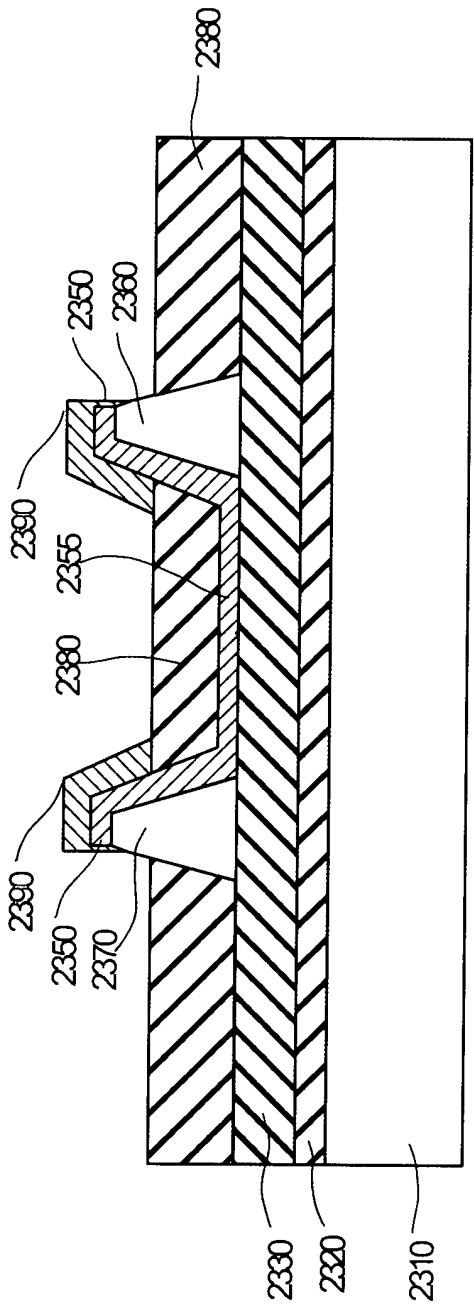


FIG. 23A

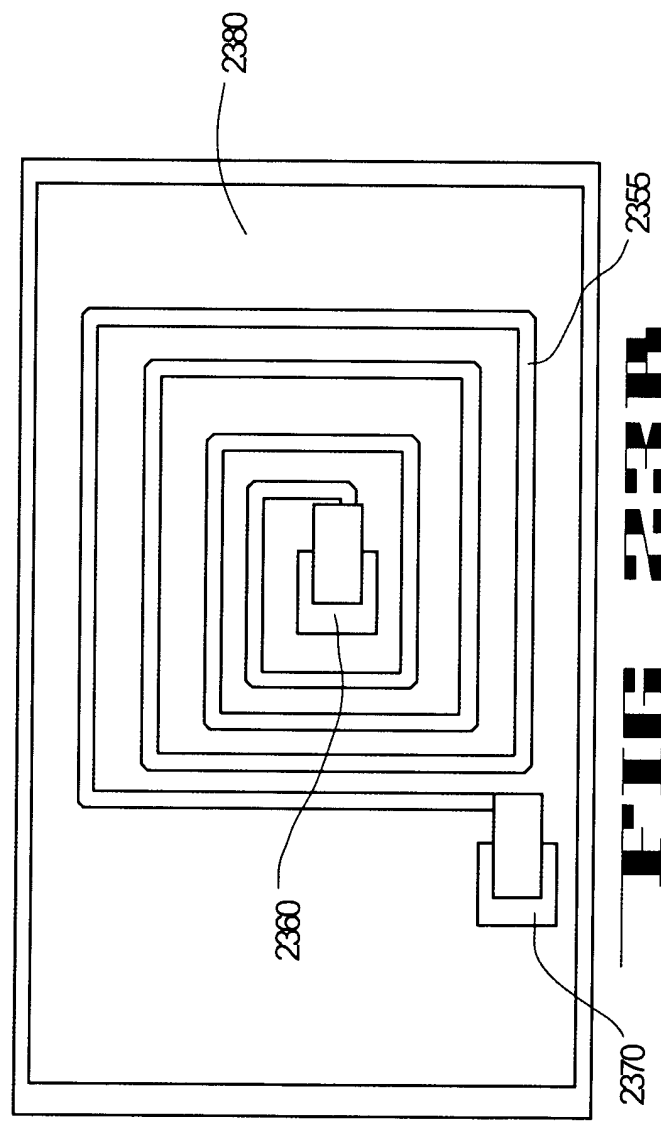


FIG. 23B